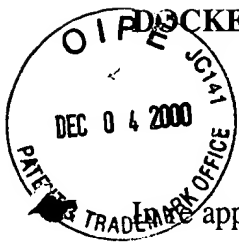


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DOCKET NO. CHADHA 1-1-1-1

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In application of:

MANDEEP SINGH CHADHA, et al.

Serial No.: 09/650,851

Filed: August 29, 2000

For: FILTER CIRCUIT FOR A BIT PUMP AND
METHOD OF CONFIGURING THE SAME

Group No.: 2681

Examiner: N/A

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12/01/00
(Date of Signature)

Sir:

INFORMATION DISCLOSURE STATEMENT

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement. This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The patents, publications and other information herein are listed below and on the attached Form PTO-1449. Copies of the listed references are submitted herewith.

K. H.

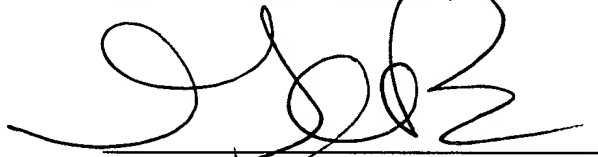
References

"Multirate Systems and Filter Banks," by P. P. Vaidyanathan,
Prentice Hall Signal Processing Series, Chap. 5, Section 5.3, pp.
211-213, July 1992

Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above Patents and to question the relevance and materiality of the Patents and Publications listed herein, in whole, in part, or in combination, subsequent to filing this Information Disclosure Statement.

Respectfully submitted,

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